PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

19 C

In re the Application of:

TANIGUCHI, et al

Serial No.:   09/266,869

Filed: March 12, 1999

For: MICROCONTROLLER HAVING PREFETCH FUNCTION

Confirmation No.: 6342

Art Unit: 2653

Examiner: Gautam R. Patel

Docket No.: 108075-09008

5/14/03

PRELIMINARY AMENDMENT

Commissioner for Patents
Washington, D.C. 20231

May 9, 2003

Sir:

Prior to initial examination of the application, please amend the above-identified

application as follows:

IN THE CLAIMS:

Please amend claims 1, 3, 8, 9, 14 and 24-34 as follows:

1.   (Thrice Amended) A method for prefetching instructions and data of a

program stored in a memory, wherein the program includes a pseudo instruction and at

least one instruction, the pseudo instruction being arranged before the at least one

instruction and including an address for the at least one instruction or an address for

data, the method comprising the steps of:

reading the program from the memory;

detecting the pseudo instruction with a first unit;

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